SUPPLE MENTAL Notice of Allowability

Application No.	Applicant(s)		
09/851,885	KUBOTA ET AL.		
Examiner	Art Unit		
Vincent E. Kovalick	2673		

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	Vincent E. Kovalick	2673	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to <u>applicant's amendment</u>	nt to Final Action dated 5/10/.05.		
2. The allowed claim(s) is/are <u>1-81, 88-90 and 118-133</u> .			
3. \boxtimes The drawings filed on $\underline{\it 09May2001}$ are accepted by the Ex	aminer.		
 4. Acknowledgment is made of a claim for foreign priority un a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 			
Certified copies of the priority documents have Certified copies of the priority documents have			
Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).	• • • • • • • • • • • • • • • • • • • •		tion from the
* Certified copies not received:	·		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the red	quirements
5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give			OTICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.		
(a) \square including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date	·		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the O	ffice action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written on the drawin he header according to 37 CFR 1.121(c	gs in the front (not the l).	back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I			Note the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Informal Pa	atent Application (PT	D-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Linterview Summary Paper No./Mail Date	(PTO-413),	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4/19/04 & 9/1/04	8), 7. Examiner's Amendm	nent/Comment	
4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛭 Examiner's Stateme	nt of Reasons for Allo	wance
of Biological Material	9. Other		

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SUPPLEMENTAL

DETAILED ACTION

Response to Amendment

1. This Office Action is in response to Applicant's Amendment dated May 10, 2005, in response to a phone interview regarding claim numbering with Applicant's Attorney, Mr. William J. Daley, Jr. Reg. No. 35,487. on May 10, 2005.

EXAMINER'S AMENDMENT

- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 3. Authorization for this examiner's amendment was given in a telephone interview with Applicant's Attorney, Mr. William J. Daley, Jr. Reg. No. 35,487 on May 10, 2005.

 Please modify the following claims:
 - In claim 2, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:--- In claim 2, line 1;
 - In claim 3, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---
 - In claim 4, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---

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- In claim 5, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---
- In claim 6, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---
- In claim 11, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---
- In claim 12, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---
- In claim 13, line 1; following the words "defined in" delete the words "claims 1 or 122, wherein:" and insert the words ---claim 1, wherein:---

Please add the following new claims 126-133:

126. (New) The image display device as defined in claim 122, wherein:

only one of the parts and entireties of the drive circuit(s) operates at any given time.

127. (New) The image display device as defined in claim 122, wherein:

the same part(s) and entirety(ies) of the drive circuit(s) is(are) driven throughout one or more frame periods

128. (New) The image display device as defined in claim 122, wherein:

two or more of the parts and entireties of the drive circuit(s) are switchable driven in one frame period.

129. (New) The image display device as defined in claim 122, wherein:

at least two of the parts and entireties of the drive circuit(s) write image data in respective areas on a screen.

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130. (New) The image display device as defined in claim 122, wherein:
a part or entirety of the data signal line drive circuit is provided in plurality; and
at least two of the parts and entireties of the data signal line drive circuit write image data
in one partial or whole area on a screen in one frame period.

- 131. (New) The image display device as defined in claim 122, wherein:
 a part or entirety of the data signal line drive circuit is provided in plurality; and
 at least one of the parts and entireties of the data signal line drive circuit writes image
 data in a blanking period of each horizontal scan period.
- 132. (New) The image display device as defined in claim 122, wherein:
 a part or entirety of the data signal line drive circuit is provided in plurality; and
 at least one of the parts and entireties of the data signal line drive circuit writes image
 data with a predetermined delay from another part or entirety of the data signal line drive
 circuit.
- 133. (New) The image display device as defined in claim 122, wherein: the parts and entireties of the drive circuit(s) are located opposing one another across the pixel array.

Allowable Subject Matter

- 4. Claims 1-81, 88-90 and 118-133 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

Relative to claims 1, 122 and 123, the major difference between the teachings of the prior art of record (USP 5,973,661, Kobayashi et al. and USP 5,638,501, USP 5,638,501) and that of the

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instant invention is that said prior art of record **does not teach** an image display device wherein a part or entirety of either or both of the data signal line drive circuit and the scan signal line drive circuit is provided in plurality, the part including a voltage select-supplying section for selecting and supplying voltages each of which is supplied during one horizontal period to each data signal line so as to realize mutually different display configurations.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. Prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U. S. Patent No.	6,232,949	Imamura
U. S. Patent No.	6,181,317	Taguchi et al.
U. S. Patent No.	5,973,661	Kobayashi et al.
U.S. Patent No.	5 638 501	Gough et al

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Responses

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent E. Kovalick whose telephone number is 703 306-3020. The examiner can normally be reached on Monday-Thursday 7:30- 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703 305-4938. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vincent E. Kovalick

May 11, 2005

BIPIN SHALWALA
SUPERVISORY PATENT EXAMINER

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